



**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants: Apurva Kumar et al.

Docket No. JP920000365US1

Serial No. 09/785,577

Group Art Unit: 2661

Filed: February 16, 2001

Examiner:

Batch No.:

Confirmation No.: 4250

For: REDUCTION OF DEVICE DISCOVERY DELAYS IN FREQUENCY  
HOPPING-BASED AD-HOC NETWORKS

Commissioner for Patents  
Washington, D.C. 20231

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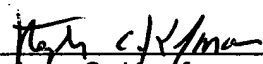
**SUBMISSION OF FORMAL DRAWINGS**

Technology Center 2600

Sir:

Submitted herewith are fourteen (14) Formal Drawing sheets for filing in the  
above-identified application.

Respectfully submitted,

  
\_\_\_\_\_  
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Telephone: (914) 945-3197

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Date: December 4, 2001



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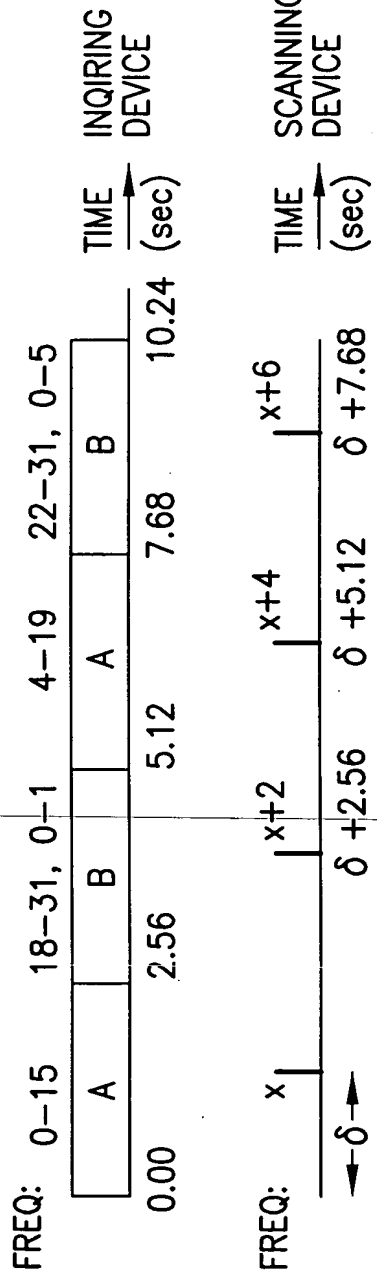


FIG.1

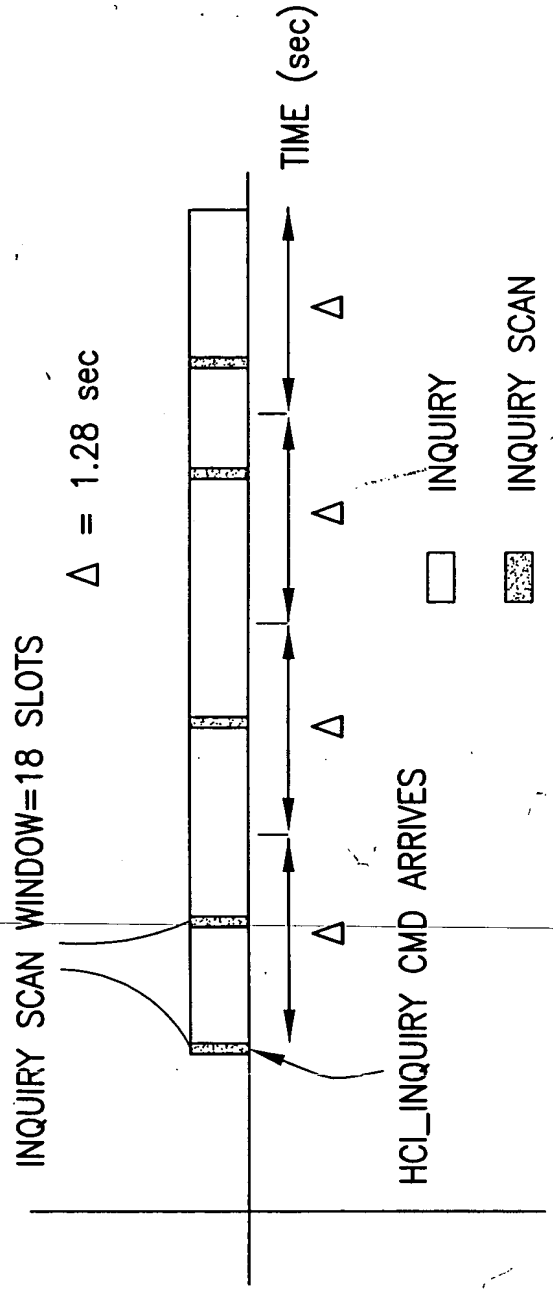


FIG.2

SYSTEM Ad\_Hoc\_System

SIGNAL\_INQ\_MSG;  
SIGNAL\_HCI\_INQUIRY;  
SIGNAL\_FHS;  
SIGNALLIST INQ=INQ\_MSG, FHS;

FIG.3b

SYSTEM Ad\_Hoc\_System

FIG.3a

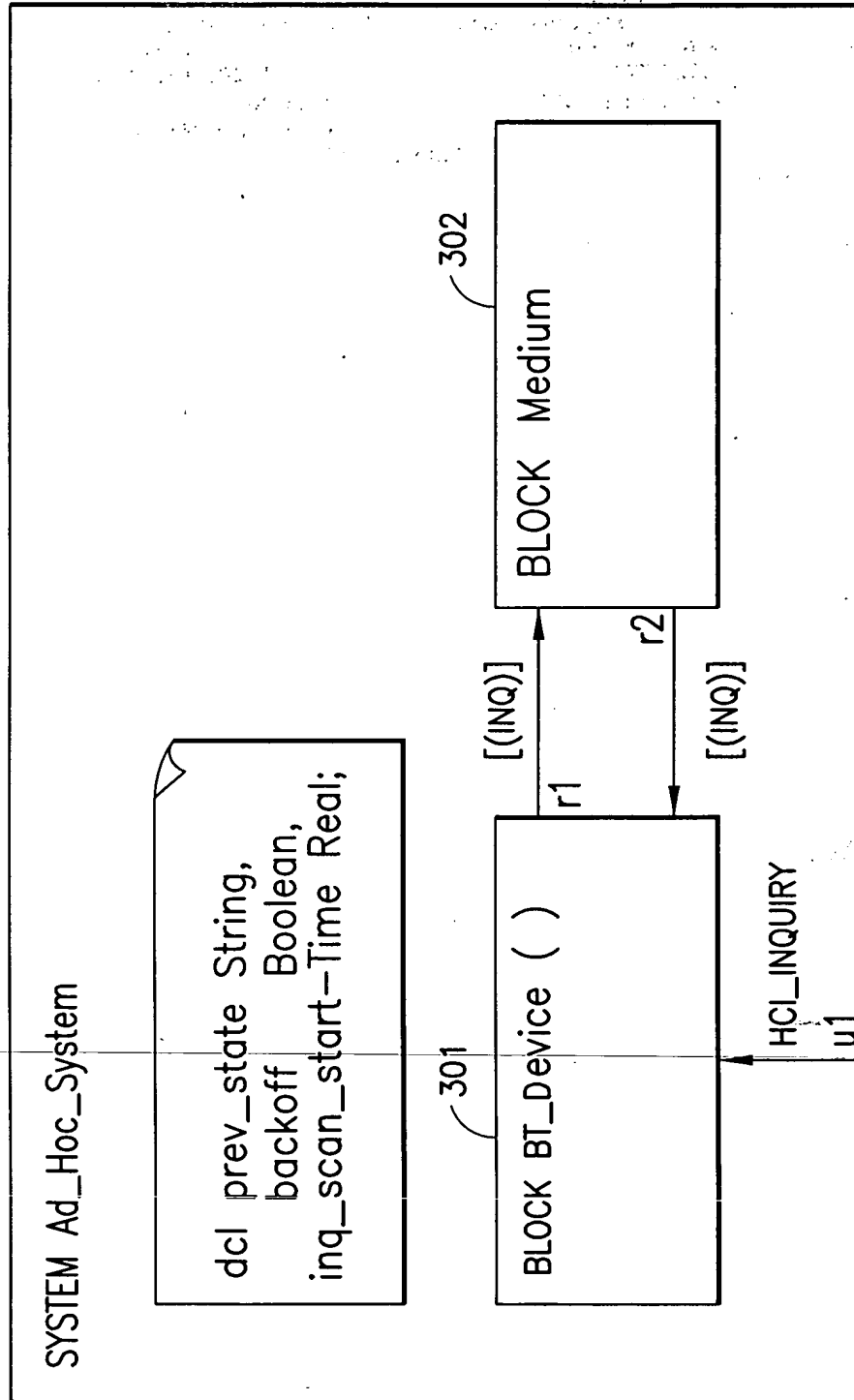


FIG.3c

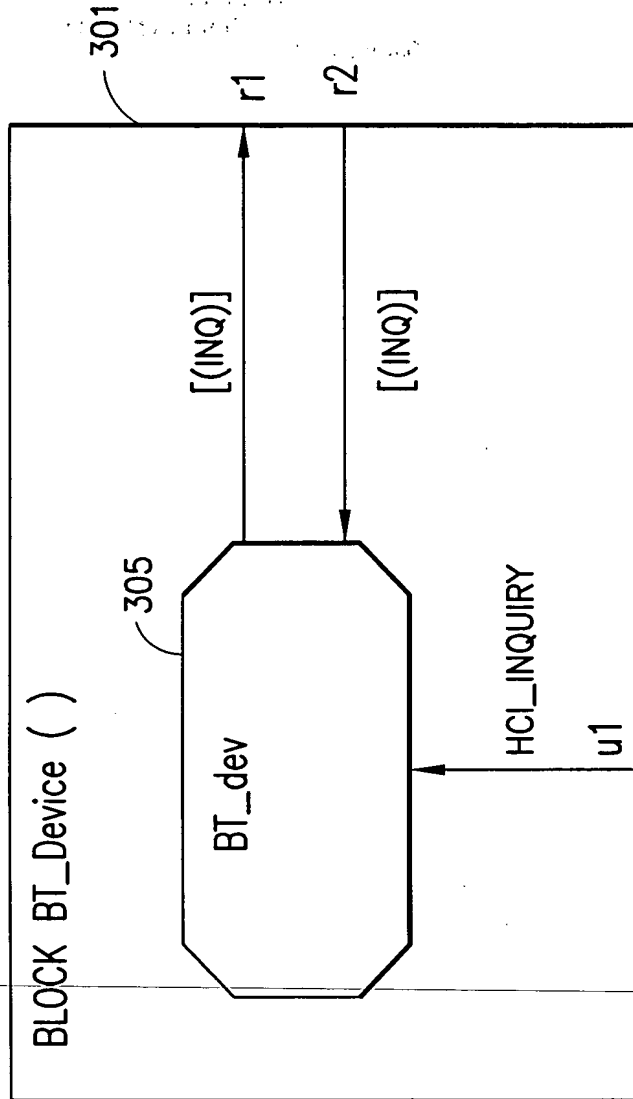


FIG.3d

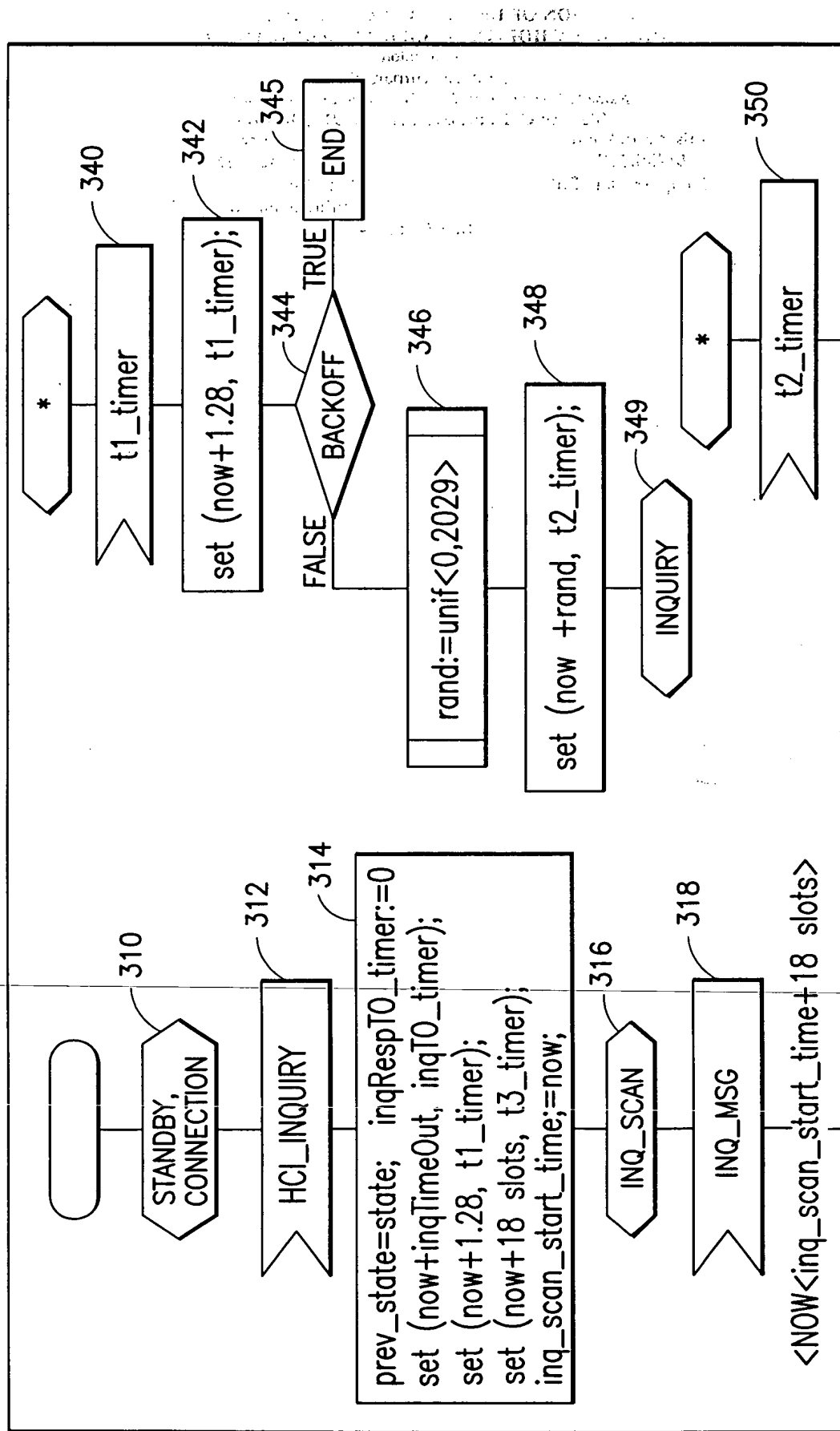


FIG.3e-1

FIG.3e-1
FIG.3e-2
FIG.3e-3

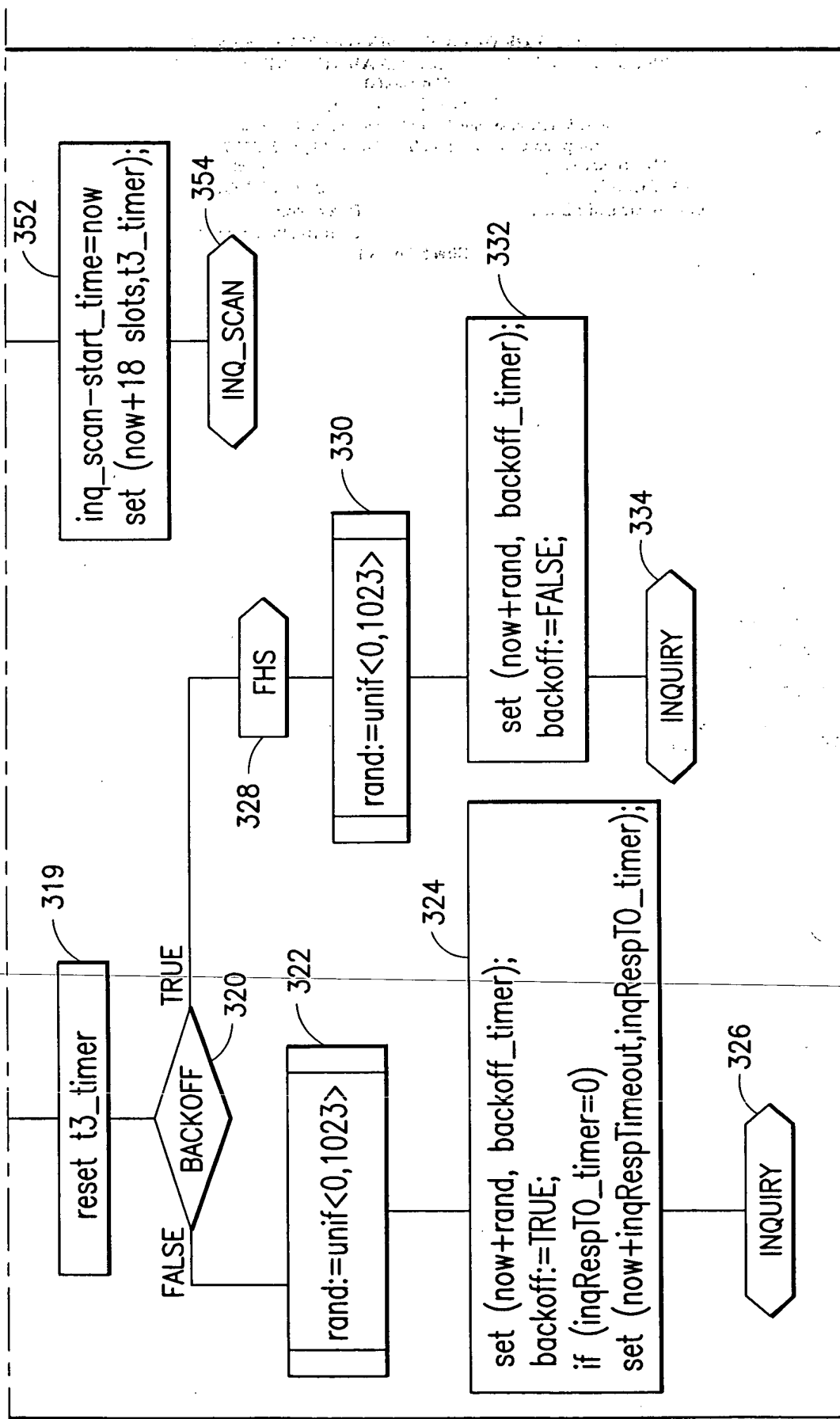


FIG.3e-2



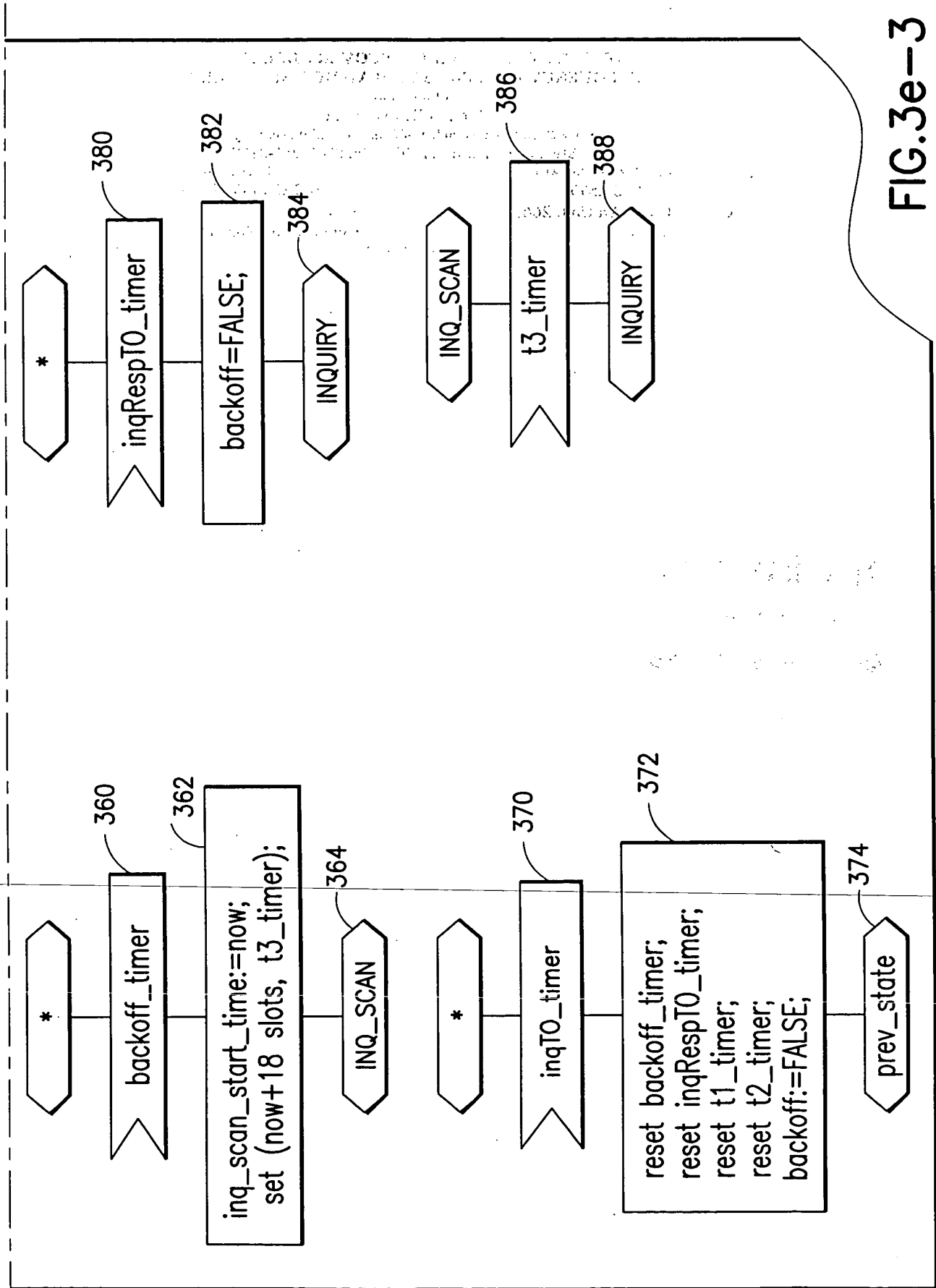


FIG. 3e-3

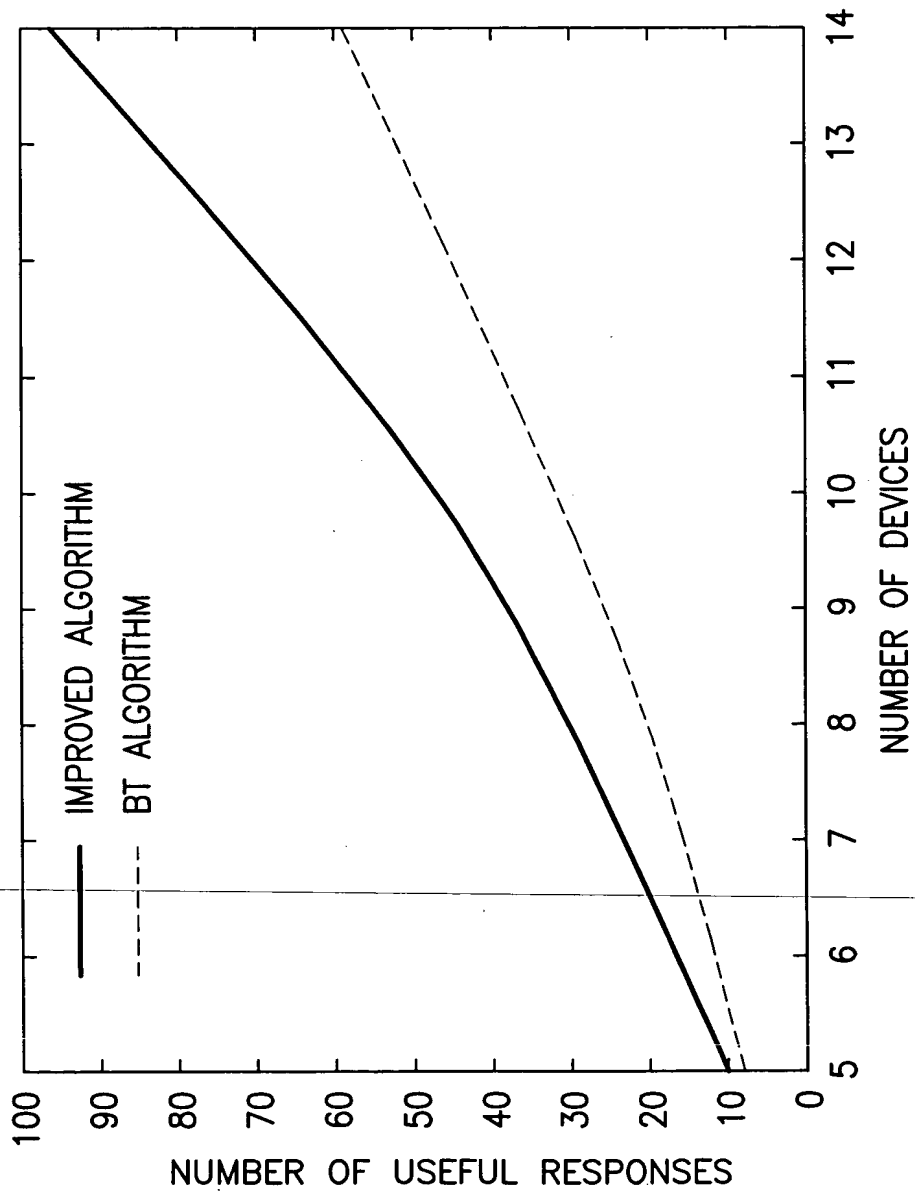
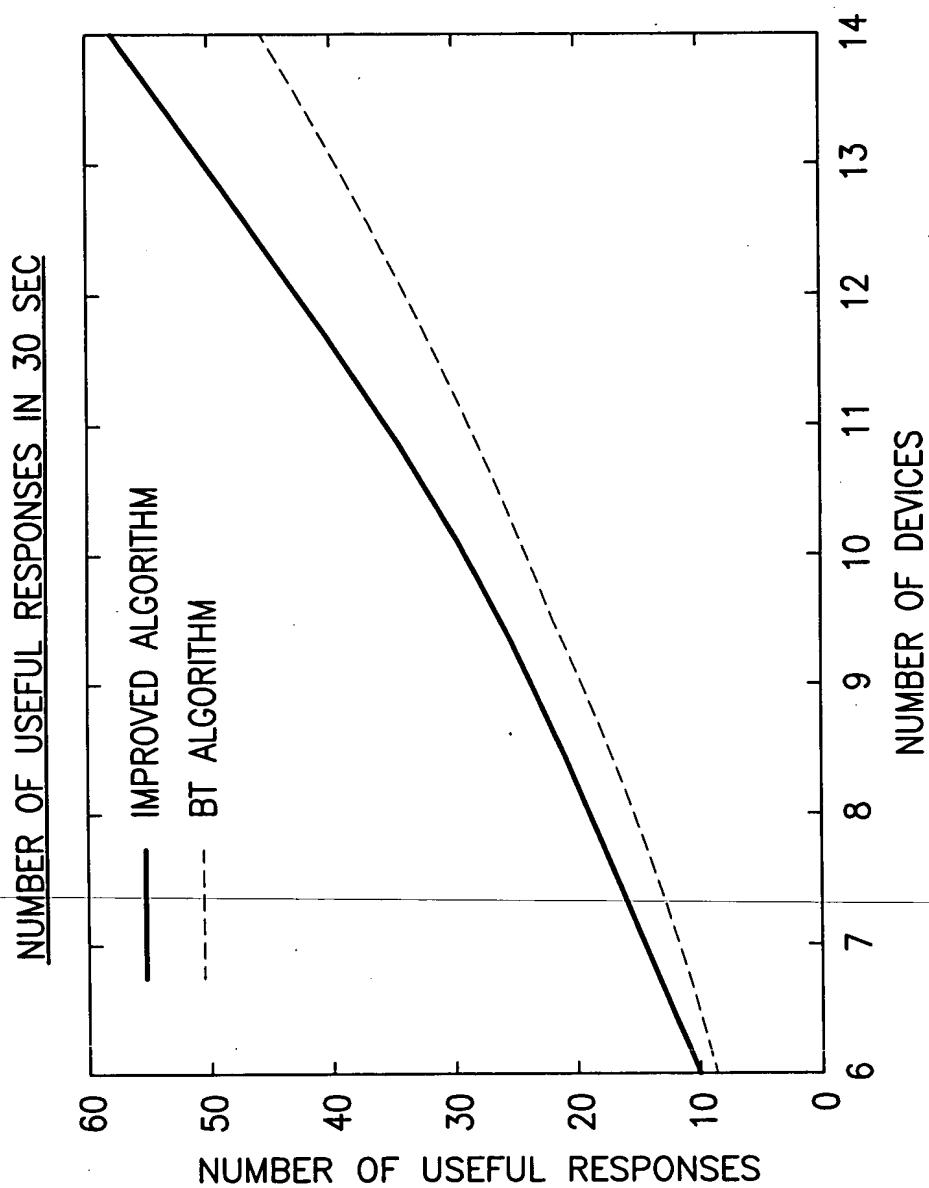


FIG.4



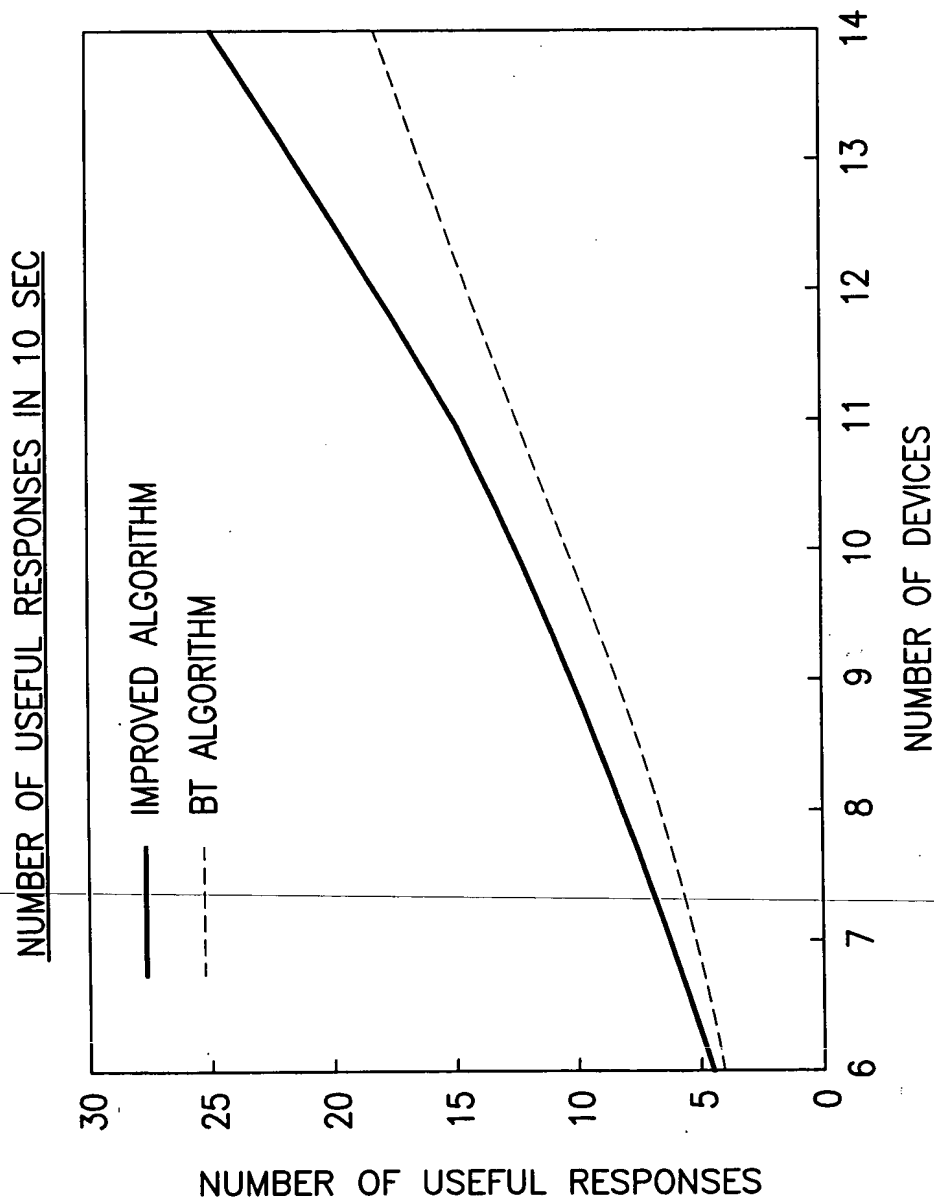


FIG.6

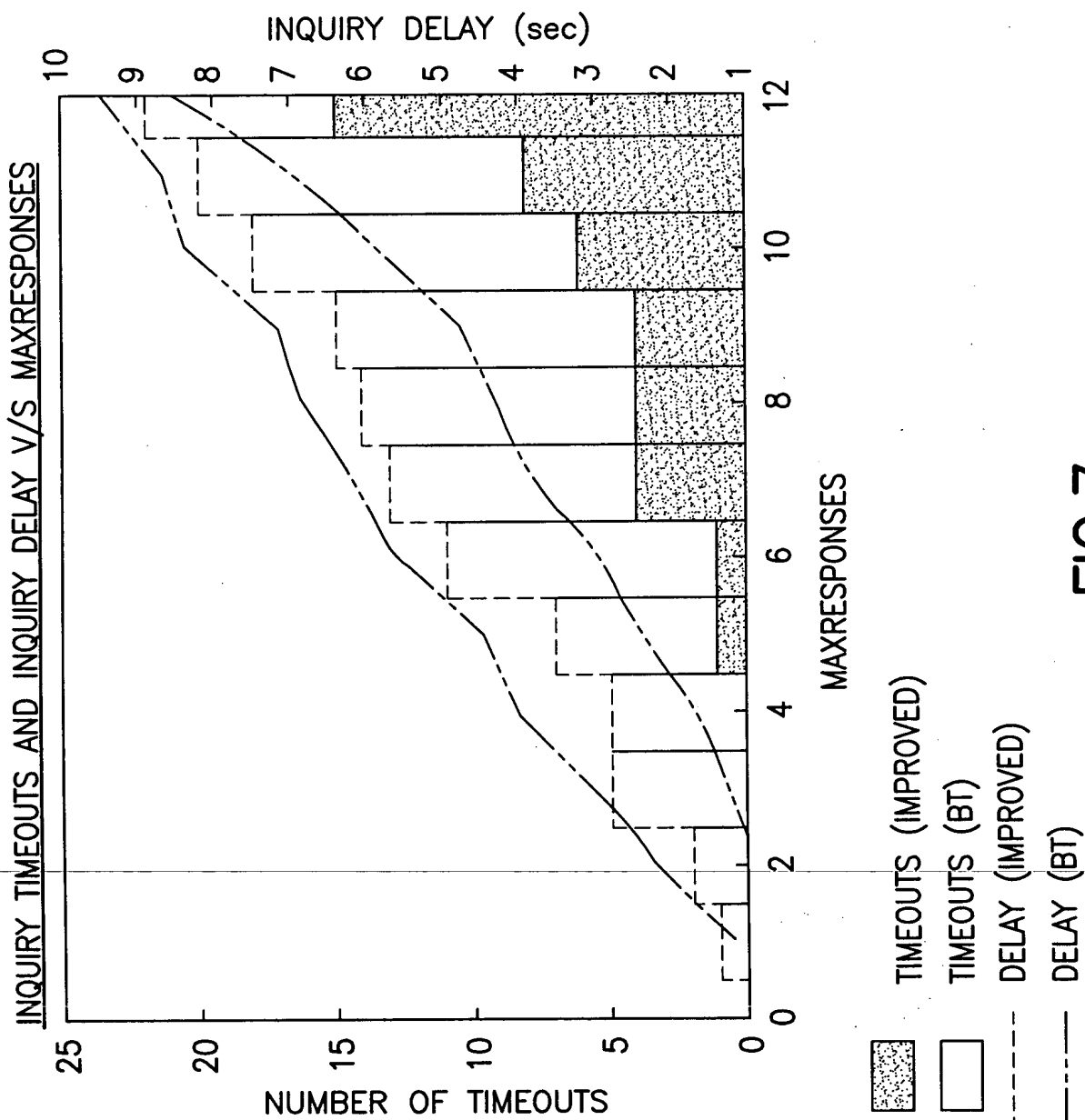


FIG.7

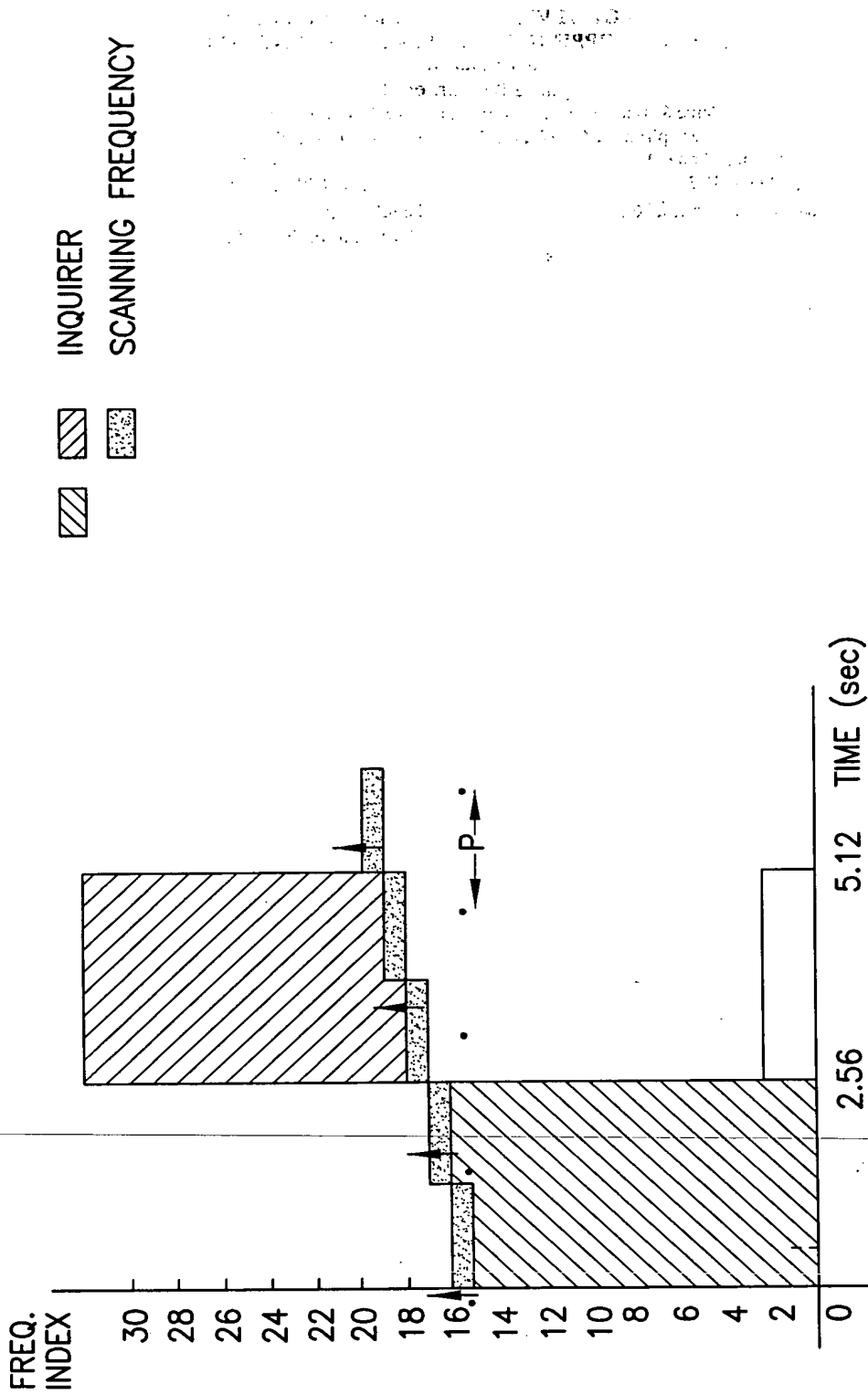


FIG.8

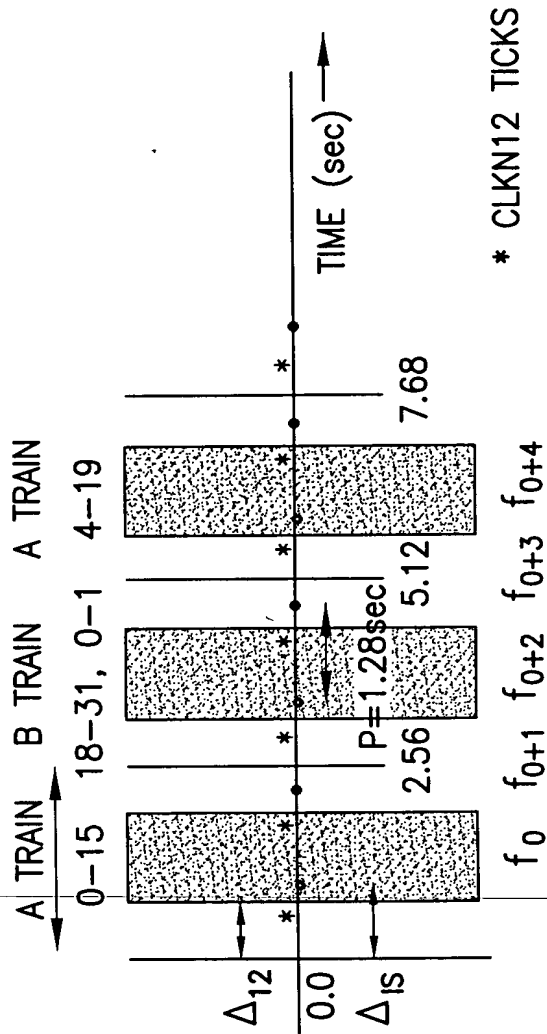


FIG.9

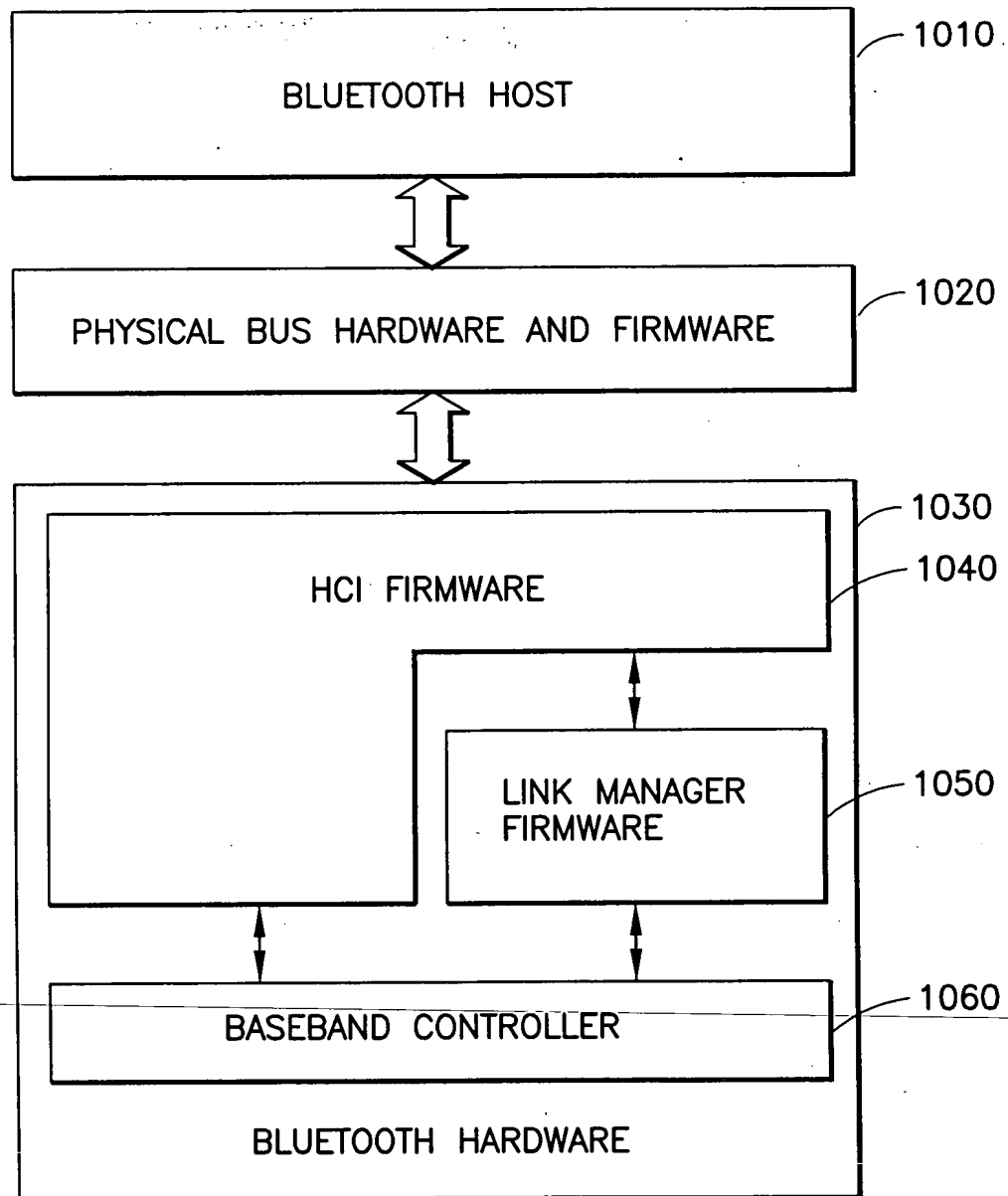


FIG.10